

PATENT ABSTRACTS OF JAPAN

(11)Publication number : **11-055535**

(43)Date of publication of application : **26.02.1999**

(51)Int.Cl.

H04N 1/60

G06T 5/00

H04N 1/46

(21)Application number : **09-206649**

(71)Applicant : **CANON INC**

(22)Date of filing : **31.07.1997**

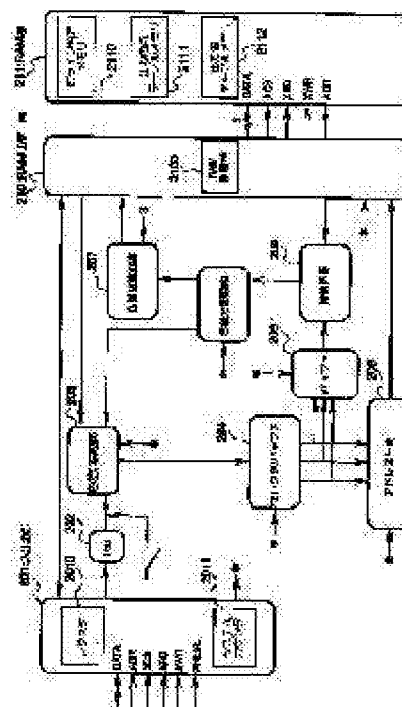
(72)Inventor : **IKEDA TORU**

(54) IMAGE PROCESSOR

(57)Abstract:

PROBLEM TO BE SOLVED: To improve the color reproducibility of an entire output image without increasing the processing load in the image processor that obtains binary image data used for an output device based on multi-value input image data.

SOLUTION: A color difference adder circuit 203 adds a color difference of a just precedingly processed pixel and a pixel at a prescribed position of a preceding line caused by binarization at a prescribed rate to multi-value input image data received via an input output section 201 for each pixel, an address generating circuit 205 generates an address based on the input image data to which the color difference is added and binary data used for print at a print-section are read from an output color table 2112 where cross reference data are stored to minimize a distance in a color space between the input image data and measured data based on the binary data.



LEGAL STATUS

[Date of request for examination]

02.08.2004

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's
decision of rejection]

[Date of extinction of right]